

E157 Lecture 22 Day Plan

Any questions before quiz

Quiz + Team Quiz + Talk through solution

Quantization noise

- Clarification on quantization noise stdev – it's also OK to say $\sigma_q = LSB/\sqrt{12}$
- What is the LSB size of an ADC with Nbits and Vfs?
 - $LSB = Vfs / [2^{(Nbits)-1}]$, note you need a fencepost at VFS and at 0
- SQNR = 6.02dB/bit + 1.76dB
 - $P_{sig} = (Vfs/2)^2/2R \rightarrow$ "stdev sig" = $Vfs/(2*\sqrt{2})$
 - "stdev quant" = $LSB / \sqrt{12}$
 - Dynamic Range = stdev sig / stdev quant = $\sqrt{12}/(2*\sqrt{2}) * (2^{Nbits} - 1) = \sqrt{3/2}...$
 - $SQNR = (Dynamic\ Range)^2$
 - $SQNR_{dB} = 2*10*\lceil \log(\sqrt{3/2}) + Nbits*\log(2) \rceil$ (ignoring the -1)

Processing Gain – a FFT sets the noise bandwidth

- In an FFT, each f_0 captures frequencies from $kf_0 - f_0/2$ to $kf_0 + f_0/2$.
- Therefore, $P_n = kTB$ will often have B set by the value of f_0
- If f_0 is narrower than the narrowest bandpass filter, f_{BP} , then you have an apparent "gain" in SNR, because the noise floor is suppressed by the narrow FFT bandwidth

Are oscilloscopes limited by quantization or thermal noise?

<https://www.keysight.com/en/pdx-x201837-pn-DSOX2024A?nid=-32542.1150190&cc=US&lc=eng>

8 bits, 200MHz front-end BW into 1MHz termination, smallest V/div is 10mV on 10 divs

- $P_n = kTB = (4.14 \times 10^{-21} \text{ J}) * 200\text{MHz} = 8.28 \times 10^{-13} \text{ W}$
- $v_n^2 = 4 * R * P_n = 3.31 \times 10^{-6} \text{ V}^2$
- $v_n = 1.8 \text{ mV}$
- $v_q^2 = LSB^2/12 = (100\text{mV}/(256-1))^2/12 = 1.28 \times 10^{-8} \text{ V}^2 \rightarrow v_q = 0.113 \text{ mV @ tightest zoom}$
- Steeping out to wider gains increases v_q , but also introduces more vertical gain to amplify v_n

Noise spreadsheet

- Carrier frequency is 2.4GHz
- Chain goes: T/R switch – amp – filter – amp
- Amplifiers: <https://www.minicircuits.com/pdfs/ZRL-3500+.pdf>
- Filter: <https://www.minicircuits.com/pdfs/VBF-2435+.pdf>
- T/R Switch: <https://www.minicircuits.com/pdfs/ZFSWA-2-46.pdf>
- Fill in spreadsheet below, sketch the spectrum at the output of this system, and pick how many ADC bits and what ADC sample rate you want.

Noise spreadsheet:

https://docs.google.com/spreadsheets/d/1a8tp4q8aZqqk_zcnzNJ1Xu8owhBuvWTrAU_nkpJs9Y8/edit?usp=sharing