

E151 Lecture 21 Handout

What is offset voltage and where does it come from?

What is the difference between input-referred and output-referred offset voltage?

Draw an amplifier model that includes an input offset voltage, an input offset current and an input bias current

What's the easy way to take a single ended output from an emitter coupled pair? A better way?

What are the input impedance and voltage gain of a current mirror loaded emitter coupled pair?

What's the output impedance of a current mirror loaded emitter coupled pair?

Extra recall:

In an amplifier do differential signals have the same bandwidth as common mode signals?