

E151 Lecture 8 Handout

What limits output voltage swing in a common emitter amplifier on the high side? The low side?

How does changing collector current in your amplifier result in distortion even when the amplifier is in forward active?

How can you calculate the percent distortion in a common emitter amplifier?

Record the design process for a simple common emitter amplifier

In a standard common emitter amplifier, where would you usually find “easy” constraints for the design process? What are common easy constraints?