

E155 Project Rubric

Designers: _____

Checkoff

Project meets specifications	_____ / 10
Polish	_____ / 4
Difficulty	_____ / 3
Schematics	_____ / 2
Software clean and efficient	_____ / 3
Verilog clean and efficient	_____ / 3
Total	_____ / 25

Report

Abstract	_____ / 2
Introduction: Motivation, Block Diagram, Overview	_____ / 3
New Hardware	_____ / 3
Schematics	_____ / 1
Microcontroller Design	_____ / 2
FPGA Design	_____ / 2
Results	_____ / 3
References	_____ / 2
Bill of Materials	_____ / 2
Software	_____ / 1
Verilog	_____ / 1
Writing & Organization	_____ / 3
Total	_____ / 25