



**else's work.** Solutions to past years labs have been handed out. Obviously, it is also an honor code violation to refer to these solutions while doing your lab. The final project will be done in groups of two.

The deliverables for each lab will consist of the following:

- A brief written report, which must not exceed one page, that provides a summary of the work, context for the other materials, an explanation of major design decisions, and a succinct explanation of whether the lab met requirements or how it deviates.
- Schematics of the circuits constructed for the lab. Use standard symbols for standard components, and be sure the schematic is sufficient for another engineer to reproduce your circuit.
- Code written for the lab.
- Figures that illustrate the output of the circuits or other results of the lab.

The purpose of these deliverables is to train you in professional, technical communication, so there are high standards to which they will be held. All schematics must feature neat wiring, up to date values for every component, and ports labeled with both the pin and the signal associated with them. Figures must be readable and must include labeled axes and legends. Code should be commented; omit library code that is provided elsewhere. The report should be grammatically correct, succinct, tightly focused and thorough. Properly referencing figures will help keep the report brief.

Labs are graded on a 7-point scale. 3 points are given for the system meeting its specified requirements. Another point is given for answering a “fault tolerance question”. 3 points are given for the report, schematics, and code.

Wednesday lectures will mostly consist of in-class activities and design projects. Your work will count toward a small portion of your grade. You may miss two days without penalty.

## Schedule

Week	Monday Lecture	Wednesday Activity	Due
8/28		Analog Behavior of Digital System	
9/4	Comb & Seq Logic	Logic Design	(Lab Demos)
9/11	Verilog Coding	FPGA Datasheet	Lab 1 – FPGA Board
9/18	Synchronous Design	Verilog FSMs	Lab 2 – Muxed Display
9/25	Assembly Language	Microcontroller Programming	Lab 3 – Keypad
10/2	Peripherals	C Programming	Lab 4 – Assembly
10/9	Serial Interfaces	Embedded Web Server <Spencer>	Lab 5 – Audio
10/16	Fall Break No Class		
10/23	AES	Digital System Architecture	Lab 6 – Internet of Things
10/30	Project Kickoff	Motors, Speakers	Lab 7 – AES Project Proposal 11/1
11/6	Wireless Links	Pi and VGA Graphics	Proposal Debriefs
11/13	Patents	TBD	
11/20	Presentations	Happy Thanksgiving! No Class.	Project Status Demo
11/27	Presentations	Presentations	
12/4	Interview Questions	Project Demos	Report Due 12/8

## Honor Code Violations

It goes without saying that students in this class are expected to follow the honor code. An honor code policy appears below and prescribes behavior that is considered honorable, so read those maxims and follow them closely. Any honor code violations will be handled through JB.

## Honor Code Policy

1. All students enrolled in this course are bound by the HMC Honor Code. More information on the HMC Honor Code can be found in the HMC Student Handbook.
2. It is your responsibility to determine whether your actions adhere to the HMC Honor Code. If this document does not clarify the legitimacy of a particular action, you should contact the course instructor and request clarification.
3. Work you submit for individual assignments should be your own, and you should complete all assignments based on your own understanding of the underlying material. If you work with, or receive help from, another individual on an assignment, provide a written acknowledgement in complete sentences that includes the person's name and the nature of the help.
4. This document is not meant to be an exhaustive list of every possible Honor Code violation. Infractions not explicitly mentioned here may still violate the Honor Code.
5. **Boundaries of Collaboration**  
Verbal collaboration with other students on individual assignments is encouraged AFTER you have given serious thought to each component yourself. However, all submitted written work should be written by yourself individually, and not a collaborative effort or copied from a common source (e.g., a chalkboard). It is NOT acceptable to work on labs in lockstep with another classmate.
6. **Use of Computer Software**  
The use of graphing calculators and computer software to aid in course work is acceptable, as long as it does not substitute for an understanding of the course material.
7. **Use of Web Resources**  
The use of Internet resources to aid in course work is acceptable, as long it does not substitute for an understanding of the course material. Plagiarism and direct copying from online (or any other) sources is strictly prohibited.
8. **Use of Your Own Work from Previous Semesters**  
If you have previously attempted this course, you may resubmit your work from previous semesters as this semester's coursework, as long as you understand the underlying material.
9. **Use of Other Course Resources from Previous Semesters**  
You may not reference assignments (labs, problem sets, activities) of this course from previous semesters.
10. **Retention of Course Resources**  
Assignments and exams from this course may not be committed to dorm repositories or otherwise used to help future students.

## Inclusiveness and Harassment

We do difficult work in this class and everyone should feel comfortable engaging with the material. We explicitly want students to feel safe doing this work, so it is worth stating that the instructors are committed to making the class a safe space for everyone regardless of race, gender, ethnicity, sexual orientation, religion and especially academic history. If you feel that you are experiencing a hostile environment, speak to an instructor immediately.