Design Project 1 Report Template

Names:

|  |  |  |  |
| --- | --- | --- | --- |
| Parameter | Analytical | Simulated | Measured |
| Gain |  |  |  |
| Input Resistance |  |  |  |
| Output Resistance |  |  |  |
| Output Voltage Swing |  |  |  |
| Low Frequency Corner |  |  |  |
| Power Consumption |  |  |  |
| Stage 1 Collector Current |  |  |  |
| Stage 2 Collector Current |  |  |  |
| Stage 3 Collector Current |  |  |  |
| Stage 4 Collector Current |  |  |  |

(You may fill in N/A for any measurements that are no applicable to your circuit – eg: no 4th stage)

Schematic of your circuit with labeled component values   
(Draw by hand or use circuit program, LTSpice schematics are too hard to read)

Input and output traces showing normal amplifying operation

Input and output traces showing maximum output voltage swing that meets THD spec

FFT of maximum output swing

Screenshot of LTSpice schematic

Picture of circuit

Schematic of each test setup that you used and brief explanation of test (1-2 pages)

Explanation of design process. Reference earlier schematics and use equations as necessary. (~1 page)

Explanation of discrepancies between simulated, measured and calculated values. Reference earlier schematics or results and use equations as necessary. (~1 page)