

# E151 Lecture 20 – Differential Pairs

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## Disclaimer

These are notes for Prof. Spencer to give the lecture, they were not intended as a reference for students. Students asked for them anyway, so I'm putting them up as a courtesy. Remember that they are not intended as a substitute for attending lecture.

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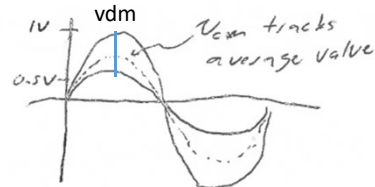
## What is Differential

- Op-amps:  $A(v_+ - v_-)$  ... how do we do that?
- Can represent any two signals as a common & differential mode
- Often useful b/c lots of noise (and distortion) is common mode

$$v_{dm} = v_1 - v_2 \quad \text{or} \quad v_{cm} = \frac{v_1 + v_2}{2} \quad \begin{array}{l} \text{instantaneous} \\ \text{(average value)} \end{array}$$

$\hookrightarrow$  (usually small signal)

can express  $v_1 = v_{cm} + \frac{v_{dm}}{2}$  ( $v_2 = v_{cm} - \frac{v_{dm}}{2}$ )

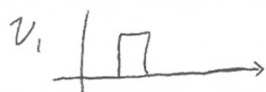


- EG:  $v_1 = 1V \cdot \sin(\omega t)$   
 $v_2 = 0.5V \sin(\omega t)$

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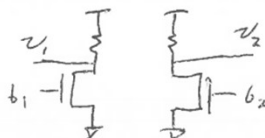
## Example of Differential Signals

You try ~~pseudo~~ <sup>pseudo</sup> diff signaling ~ a common digital technique



- You draw  $v_{cm}$  &  $v_{dm}$

- from this com link

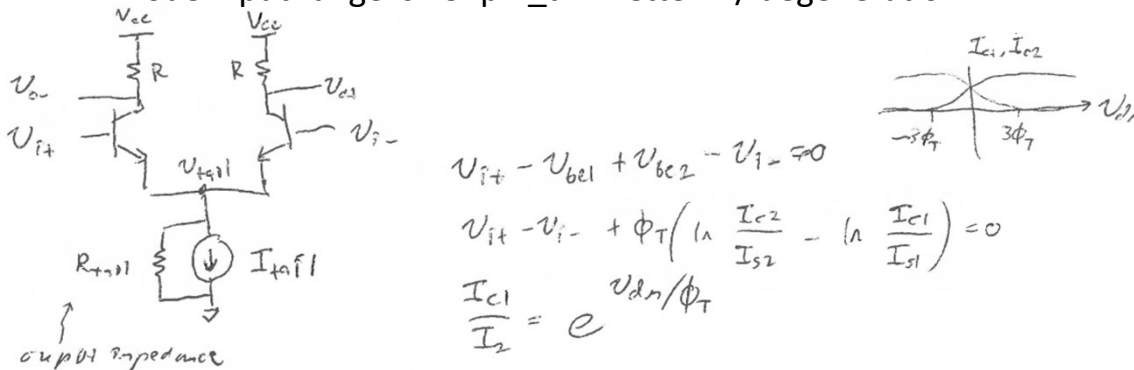


- This isn't called purely differential signaling b/c there is some CM
- How would you measure  $r_{in\_cm}$  of an amp?  $r_{in\_dm}$ ?

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# Emitter Coupled Pair + Large Signal Behavior

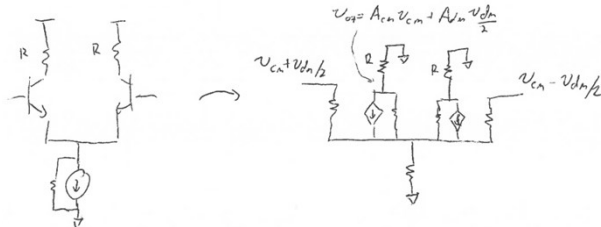
- The most common circuit that amplifies DM but not CM is ECP
- Big picture: works by current steering ... DM does and CM doesn't
- Diff mode input range is  $\sim 3 \cdot \phi_{th}$ . Better w/ degeneration.



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## 1/2 Circuit Analysis

- Analyze the small signal model w/ symmetry and superposition.



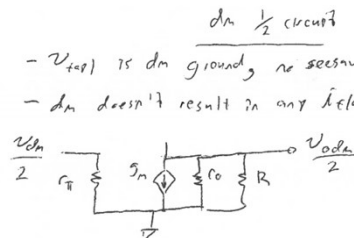
- superposition breaks into cm & dm circuits
- symmetry lets us draw 1/2 circuits

cm 1/2 circuit

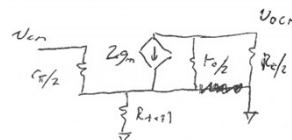
- everything is in parallel

- dm vs cm parameters -  $a_v$ ,  $r_{in}$ ,  $r_{out}$ , etc.

- $CMRR = a_{dm}/a_{cm}$



$$A_{dm} = g_m (r_o || R)$$

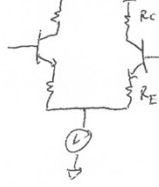


$$A_{cm} \approx \frac{R_c}{2R_{tail}} \quad \text{1/2 looks like CE w/ deg}$$

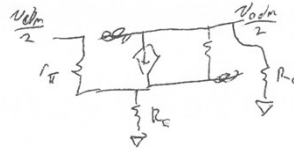
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# 1/2 Circuit Practice

- Extends linear input range to  $I_{tail} * R_E$  (same as linear design)

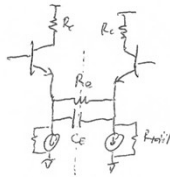


- Draw  $d_m$  1/2 circuit & find  $A_v$



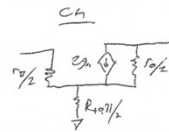
- looks like CE w/ degen  
-  $A_v \approx -\frac{R_C}{R_E}$

- An equalizer, CTLE, gain boost at high freq.
- Note 1/2 ckt OCTC and SCTC

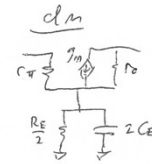


An equalizer, CTLE, experiences gain boost & we'll see why

- Find  $d_m$  &  $c_m$  1/2 ckt. Assume  $Q_1$  &  $Q_2$  identical



-  $R_E$  &  $C_e$  shunted by tail nodes @ same voltage



- differential ground in middle of  $R_E$ ,  $C_e$ 's shunt  
- Parallel Impedance  $\rightarrow \frac{R_E}{2}, 2C_e$   
noise