E151 Lecture 12 – Current Mirrors

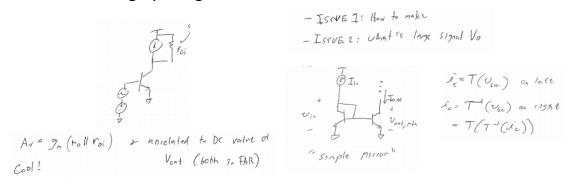
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Disclaimer

These are notes for Prof. Spencer to give the lecture, they were not intended as a reference for students. Students asked for them anyway, so I'm putting them up as a courtesy. Remember that they are not intended as a substitute for attending lecture.

What's a Current Mirror and Why?

- Active loads are pretty cool, but tough to bias
- Amount to biasing w/ a current source ... ideal load (inf. Z, inf. Vsw)
- Fix biasing by using current mirrors



What do we care about w/ mirrors?

• Mirror design specifications – functions of Ic, compare @ same Ic

ro

- V_IN
- V MIN
- Error = ε = I OUT/I IN
- rout

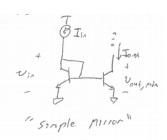
V_BEON

V_CESAT

In = I61 + Ici = (B+1) I61

beta/(beta+2)

Ion = BIL



Use Mirrors to Make Active Loads

- av = gm*(ro1||ro2) very high! (~1/2 of what we saw w/ isrc load)
- Need to pick V_B st we're in FAR, do w/ load line or another mirror

