E151 Lecture 9 – Common Emitter with Degeneration

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Disclaimer

These are notes for Prof. Spencer to give the lecture, they were not intended as a reference for students. Students asked for them anyway, so I'm putting them up as a courtesy. Remember that they are not intended as a substitute for attending lecture.

CE with Degen

• Issue: V SW \leftrightarrow av

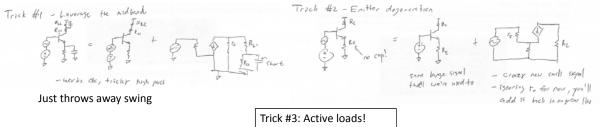
$$V_{OMAX} - V_O = I_C R_C$$

 $A_V = g_m R_C = V_{SW} / \phi_{th}$

and $rin+(V_SW \text{ or } A_V) \leftrightarrow rout$

$$r_{in} = r_{\pi} = rac{I_C}{eta \phi_{th}}$$
 Pins Ic value

- Issue for another day ... really hard to get small rout
- Can get overconstrained designs, so we need other amplifier types



CE with Degen

• Find rin, rout, av

Me Rin

Note
$$V_{in} \neq v_{ie}$$

Note $V_{in} \neq v_{ie}$

• rout = R_L if ro presumed infinite, you will do more on your HW

Small Signal Patterns

- We've just seen two common small signal models that are used a lot
- Here are more, can analyze fast if you understand / memorize
 - Thevenize aggressively, can remove from circuit
- Watch for variations: dividers to vbe, parallel stuff, ro, care w/ signs.

