

E151 Lecture 4 – Diodes in Circuits

Matthew Spencer
Harvey Mudd College
ENGR151

Disclaimer

These are notes for Prof. Spencer to give the lecture, they were not intended as a reference for students. Students asked for them anyway, so I'm putting them up as a courtesy. Remember that they are not intended as a substitute for lecture.

Diodes – why are they exponential?

- 3 descriptions: Nonlinear element, 1 way current valve, exponential
- Why exponential & 1 way?
- What's breakdown?

- what is I-V?

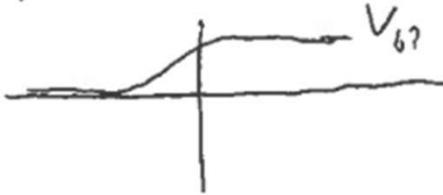
Reverse current - minority pushed across depl.

↳ push hard → e-/h+ pair, avalanche

↳ Fixed current ... limited by # carriers

Fwd current

$$-\nabla V = E$$



- E barrier is $\sim qV$

- Raise & lower w/ ext. voltage

- Thermal $E = kT$

$$I_D = I_s \left(e^{\frac{qV}{nKT}} - 1 \right)$$

$\frac{V}{n\phi_{TH}} \sim$ Thermal voltage $\sim 26mV$

Probability of transit (let $V_{bi}=0$) – carrier limited drift

Can't solve them gracefully

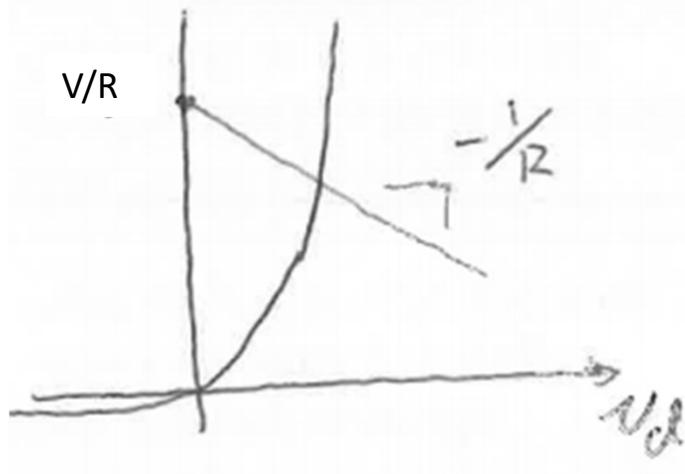
- Let's make the easiest circuit: series resistor-diode ← transcendental
- Can't even do easiest circuit! How to solve?
 - Graphically
 - Approximate
 - Linearize
 - Computers ← Don't got straight here because we want design insight



$$\frac{V - v_d}{R} = I_s \left(e^{\frac{v_d}{n\phi_{th}}} - 1 \right)$$
$$V = v_d + R I_s \left(e^{\frac{v_d}{n\phi_{th}}} - 1 \right)$$

Graphically

- One point – “Operating point” or “Bias point” – where both eqn true
- This is called load line analysis
- Good for qualitative understanding, esp. with resistive loads



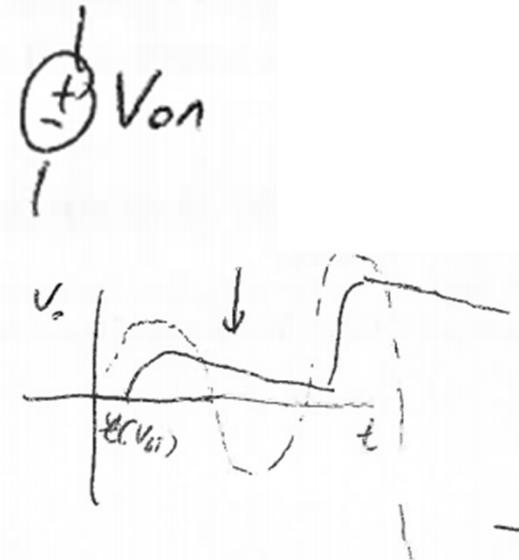
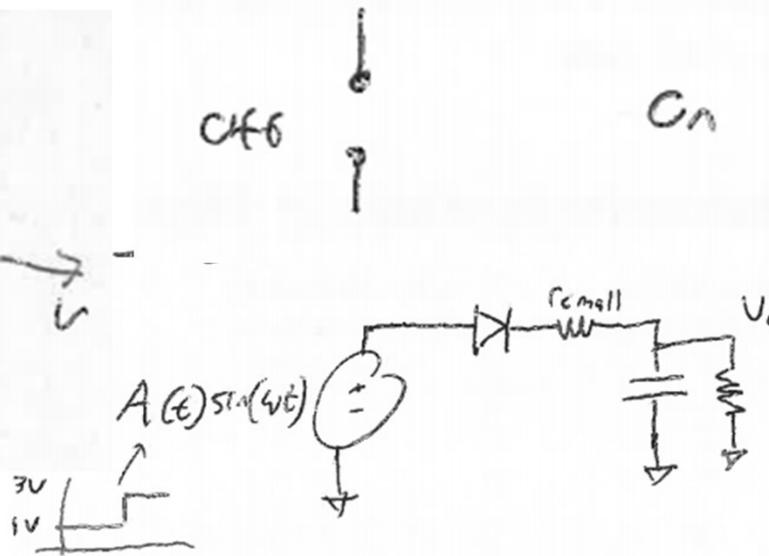
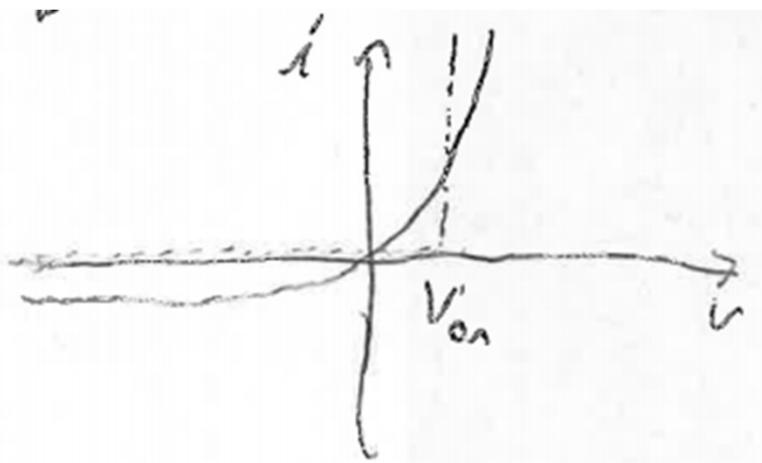
$$i = I_r \left(e^{v_d/nkT} - 1 \right) \quad \text{and} \quad i = \frac{V - v_{oc}}{R}$$

- $V_{on} = 0.7$ for Si, $1.7V$ for red, $2.3V$ for green, $3.1V$ for blue

Approximate

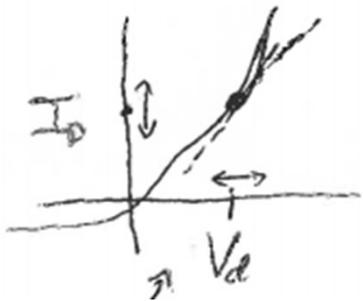
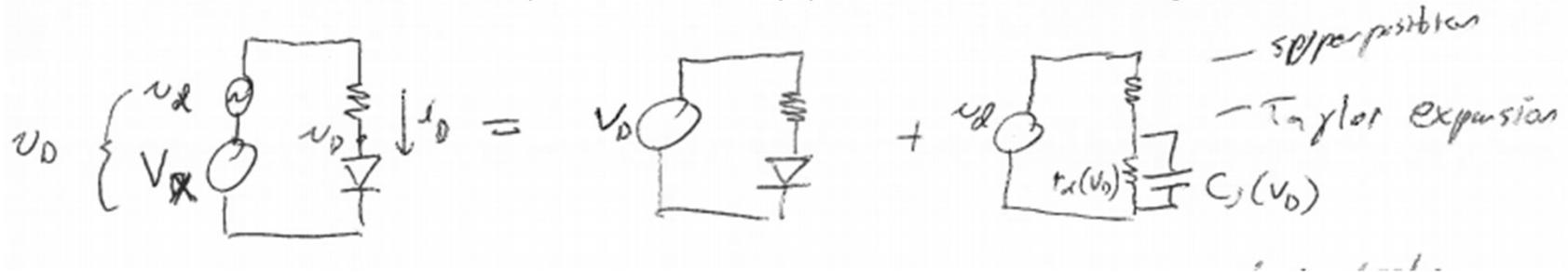
Increasing V across junction

- Exponential can be approximated by L shape: "inf current @ V_{ON} "
- (Only really works @ one current level, grain of salt)
- Switch-voltage source or switch-resistor model



Linearize → Small signal modeling

- Time varying nonlinear hard in general: $I_s \cdot \exp(qV/nkT \cdot \sin(\omega \cdot t)) \dots$
- So do linear DC (already done in approx.) + small signal linear AC



~~Small signal~~

$$f(x) \text{ near } a \approx f(a) + \left. \frac{df}{dx} \right|_a (x-a) + \frac{d^2f}{dx^2} \bigg|_a \frac{(x-a)^2}{2} \dots$$

$$i_d(v_d) \text{ near } V_D \approx i_d(V_D) + \underbrace{\left. \frac{di_d}{dv_d} \right|_{V_D}}_{\substack{\uparrow \\ I_D}} \cdot v_d + \dots$$

↑ variation from V_D

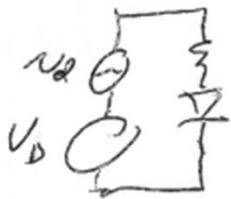
Differential Resistance

$$\frac{dI_D}{dV_D} = \frac{d}{dV_D} I (e^{V_D/n\phi_{th}} - 1) = \frac{1}{n\phi_{th}} I_s e^{V_D/n\phi_{th}} \approx \frac{I_D}{n\phi_{th}}$$

- Define this as $1/r_d \leftarrow$ differential resistance: $r_d = n \cdot \phi_{th} / I_D$
- Eq. circuit is a resistor – voltage wiggle \rightarrow linear I wiggle
- Notation: i_D (total signal) = I_D (large signal) + i_d (small signal)
- Need to shut off voltage sources b/c differential – “wiggles go to die”

If time:

example

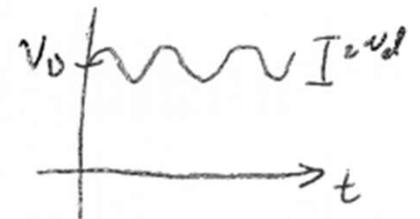


$$I_D = \frac{V_D - V_{on}}{R}$$

$$V_D = V_{on}$$

$$i_d \approx \frac{v_x}{R + n\phi_{th}/I_D}$$

$$v_{out} = \frac{n\phi_{th}/I_D}{R + n\phi_{th}/I_D} v_x$$



Computers

- Newton's method / iteration – TI-83+, Matlab, etc.
- Brute force – calculate every v_d value and pick one that matches KCL
- Circuit simulator
 - LTSpice demo
 - Components and models
 - Simulator directives
 - “You may consider this like a calculator in this class, use it throughout”
 - AC vs. DC (or .OP) vs. TRAN