Design Project 1 Report Template

Names:

|  |  |  |  |
| --- | --- | --- | --- |
| Parameter | Analytical | Simulated | Measured |
| Gain |  |  |  |
| Input Resistance |  |  |  |
| Output Resistance |  |  |  |
| Output Voltage Swing |  |  |  |
| Low Frequency Corner |  |  |  |
| Power Consumption |  |  |  |
| Stage 1 Collector Current |  |  |  |
| Stage 2 Collector Current |  |  |  |
| Stage 3 Collector Current |  |  |  |
| Stage 4 Collector Current |  |  |  |

(You may fill in N/A for any measurements that are no applicable to your circuit – eg: no 4th stage)

Schematic of your circuit with labeled component values   
(Draw by hand or use circuit program, LTSpice schematics are too hard to read)

Trace showing normal amplifying operation

Trace showing maximum output voltage swing

FFT of maximum output swing

Screenshot of LTSpice schematic

Picture of circuit

Explanation of design process (1-2 pages)

Explanation of testing methodology (1-2 pages, schematics welcome)