

*Lecture 08 -- Voltage Swing, Amplifier Design, Emitter Degeneration*

What is input leakage/bias current

What is the input bias current of a common emitter amplifier

What is the low frequency -3dB corner?

How do you calculate small signal current gain, and what is the small signal current gain of a common emitter amplifier?

What limits output voltage swing in a common emitter amplifier on the high side? The low side?

Record the design process for a simple common emitter amplifier

In a standard common emitter amplifier, where would you usually find “easy” constraints for the design process? What are common easy constraints?

What is the input resistance of an emitter degenerated common emitter amplifier?

What is the voltage gain of an emitter degenerated common emitter amplifier?