

1	17-Jan	Introduction: Digital Abstraction & Static Discipline	1.1-1.8	
10	19-Jan	Truth Tables & Logic Gates		
11	22-Jan	Boolean Expressions & Algebra	B1-2	
100	24-Jan	Transistor-level implementations		Lab 1 due
101	26-Jan	Dynamics: timing issues, hazards, K-maps		PS1 due
110	29-Jan	Sequential circuits: SR latches, D latches, flip-flops, clocking	B4-5	
111	31-Jan	Finite State Machines	B6	Lab 2 due
1000	2-Feb	Building blocks: mux, decoder, priority encoder, counter, shift reg		PS 2 due
	5-Feb	-- Intl. Solid State Circuits Conference: no class --		
1001	7-Feb	ROMs, PLAs, RAMs, FPGAs	B3	Lab 3 due
1010	9-Feb	Dynamic discipline	B7	PS 3 due
1011	12-Feb	Metastability		
1100	14-Feb	Number systems: fixed & floating point, unsigned and signed	4.1-4.2	Lab 4 due
1101	16-Feb	Arithmetic: addition and subtraction	4.3-4.5	PS 4 due
1110	19-Feb	Arithmetic: multiplication	4.6, 4.8	
1111	21-Feb	The Physics of Computation		Lab 5 due
10000	23-Feb	The Physics of Communication		PS 5 due
10001	26-Feb	Transmission Lines		
	28-Feb	Midterm		Midterm
10010	2-Mar	MIPS Instruction Set and Registers	3.1-3.4	
10011	5-Mar	Branches & Calls	3.5-3.6	
10100	7-Mar	Addressing Modes	3.7-3.8	
10101	9-Mar	Arrays & Pointers	3.9-3.15	Lab 6 due
	12-Mar	-- Spring Break: no class --		
	14-Mar	-- Spring Break: no class --		
	16-Mar	-- Spring Break: no class --		
10110	19-Mar	Linking and Launching Applications, Alternative Instruction Sets		
10111	21-Mar	Processor Datapath	5.1-5.2	Lab 7 due
11000	23-Mar	Processor Control	5.3, C1-2	PS 6 due
11001	26-Mar	Multicycle Processor	5.4, C3	
11010	28-Mar	Microprogrammed Processor	5.5, C4-6	Lab 8 due
11011	30-Mar	x86 Instruction Set emulation		PS 7 due
11100	2-Apr	Exceptions	5.6-5.10	
11101	4-Apr	Pipelining	6.1-6.3	Lab 9 due
11110	6-Apr	Pipeline hazards and stalls	6.4-6.7	PS 8 due
11111	9-Apr	Memories hierarchy: latency and throughput	7.1	
100000	11-Apr	Caches	7.2-7.3	Lab 10 due
100001	13-Apr	Memory system optimization	7.4-7.9	PS 9 due / Drop Date
100010	16-Apr	Virtual Memory		
100011	18-Apr	Performance Evaluation, or How To Lie With Statistics	2.1-2.9	Lab 11 due
100100	20-Apr	Superscalar and VLIW Processors		PS 10 due
	23-Apr	-- Presentation Days: no class --		
	25-Apr	-- Presentation Days: no class --		
100101	27-Apr	Parallel Computing	9.1-9.3	PS 11 due
100110	30-Apr	Class Summary & Review Session		
100111	2-May	Processor Slide Show		