

# **E85**

## **Digital Electronics & Computer Architecture**

### **Lecture 17:**

#### **Function Calls**



# Function Calls

- \_\_\_\_\_ calling function (in this case, main)
- \_\_\_\_\_ called function (in this case, sum)

## C Code

```
void main()
{
    int y;
    y = sum(42, 7);
    ...
}

int sum(int a, int b)
{
    return (a + b);
}
```

# Function Conventions

- **Caller:**

- passes \_\_\_\_\_ to callee
- jumps to callee

- **Callee:**

- \_\_\_\_\_ the function
- \_\_\_\_\_ result to caller
- \_\_\_\_\_ to point of call
- **must not** \_\_\_\_\_ registers or memory needed by caller

# RISC-V Function Conventions

- **Call Function:** jump and link (\_\_\_\_\_)
- **Return from function:** jump register (\_\_\_\_\_)
- **Arguments:** \_\_\_\_\_
- **Return value:** \_\_\_\_\_

Name	Register Number	Usage
<b>zero</b>	x0	Constant value 0
<b>ra</b>	x1	Return address
<b>sp</b>	x2	Stack pointer
<b>gp</b>	x3	Global pointer
<b>tp</b>	x4	Thread pointer
<b>t0-2</b>	x5-7	Temporaries
<b>s0/fp</b>	x8	Saved register / Frame pointer
<b>s1</b>	x9	Saved register
<b>a0-1</b>	x10-11	Function arguments / return values
<b>a2-7</b>	x12-17	Function arguments
<b>s2-11</b>	x18-27	Saved registers
<b>t3-6</b>	x28-31	Temporaries

# Function Calls

## C Code

```
int main() {  
    simple();  
    a = b + c;  
}
```

```
void simple() {  
    return;  
}
```

## RISC-V assembly code

```
0x00000300 main: _____, simple # call  
add s0, s1, s1  
...
```

```
0x0000051c simple: _____ # return
```

\_\_\_\_\_ means that **simple** doesn't return a value

# Function Calls

## C Code

```
int main() {  
    simple();  
    a = b + c;  
}
```

```
void simple() {  
    return;  
}
```

## RISC-V assembly code

```
0x00000300 main: jal ra, simple # call  
0x00000304           add s0, s1, s1  
...                 ...
```

```
0x0000051c simple: jr ra      # return
```

**jal ra, simple:**

---

---

**jr ra:**

---



# Function Calls

## C Code

```
int main() {  
    simple();  
    a = b + c;  
}
```

```
void simple() {  
    return;  
}
```

## RISC-V assembly code

int main()	0x00000300	main:	jal simple	# call
simple();	0x00000304		add s0, s1, s1	
a = b + c;	...		...	
void simple()	0x0000051c	simple:	jr ra	# return
return;				
}				

- Preferred instruction:

**jal simple** - a \_\_\_\_\_ for \_\_\_\_\_

- \_\_\_\_\_ are not actual RISC-V instructions but they are often simpler for the programmer.
- They are converted to real \_\_\_\_\_ instructions by the assembler



# Function Conventions

---

- **Caller:**
  - passes **arguments** to callee
  - jumps to callee
- **Callee:**
  - **performs** the function
  - **returns** result to caller
  - **returns** to point of call
  - **must not overwrite** registers or memory needed by caller

# RISC-V Function Conventions

- **Call Function:** jump and link (jal)
- **Return from function:** jump and link register (jr ra)
- **Arguments:** a0 – a7
- **Return value:** a0

# Input Arguments & Return Value

## C Code

```
int main()
{
    int y;
    ...
    y = diffofsums(2, 3, 4, 5); // 4 arguments
    ...
}

int diffofsums(int f, int g, int h, int i)
{
    int result;
    result = (f + g) - (h + i);
    return result;           // return value
}
```

# Input Arguments & Return Value

## RISC-V assembly code

```
# s7 = y
main:
    . . .
    addi a0, zero, 2    # argument 0 = 2
    addi a1, zero, 3    # argument 1 = 3
    addi a2, zero, 4    # argument 2 = 4
    addi a3, zero, 5    # argument 3 = 5
    _____               # call function
    add s7, a0, zero   # y = returned value
    . . .
# s3 = result
diffofsums:
    add t0, a0, a1      # t0 = f + g
    add t1, a2, a3      # t1 = h + i
    sub s3, t0, t1      # result = (f + g) - (h + i)
    add a0, s3, zero    # put return value in a0
    _____               # return to caller
```

**jal diffofsums**  
is pseudocode for

---

# Input Arguments & Return Value

## RISC-V assembly code

```
# s3 = result
diffofsums:
    add  t0, a0, a1    # t0 = f + g
    add  t1, a2, a3    # t1 = h + i
    sub  s3, t0, t1    # result = (f + g) - (h + i)
    add  a0, s3, zero # put return value in a0
    jr   ra           # return to caller
```

- diffofsums overwrote 3 registers: \_\_\_\_\_
- diffofsums can use the \_\_\_\_\_ to temporarily store registers

# The Stack

- \_\_\_\_\_ used to temporarily save variables
- Like stack of dishes, \_\_\_\_\_ queue
- \_\_\_\_\_ uses more memory when more space needed
- \_\_\_\_\_ uses less memory when the space is no longer needed



# The Stack

- Grows \_\_\_\_\_ (from higher to lower memory addresses)
- \_\_\_\_\_ points to top of the stack

Address	Data
BEFFFae8	AB000001
BEFFFae4	
BEFFFae0	
BEFFFadc	
⋮	⋮

← sp

Address	Data
BEFFFae8	AB000001
BEFFFae4	12345678
BEFFFae0	FFeeddCC
BEFFFadc	
⋮	⋮

← sp

# How Functions use the Stack

- Called functions must have no unintended side effects
- But `diffofsums` overwrites 3 registers: `t0`, `t1`, `s3`

## # RISC-V assembly

```
# s3 = result
diffofsums:
    add  t0,  a0,  a1    # t0 = f + g
    add  t1,  a2,  a3    # t1 = h + i
    sub  s3,  t0,  t1    # result = (f + g) - (h + i)
    add  a0,  s3,  zero # put return value in a0
    jr   ra              # return to caller
```

# Storing Register Values on the Stack

```
# s3 = result
```

```
diffofsums:
```

---

---

---

```
add t0, a0, a1
```

```
add t1, a2, a3
```

```
sub s3, t0, t1
```

```
add a0, s3, zero
```

---

---

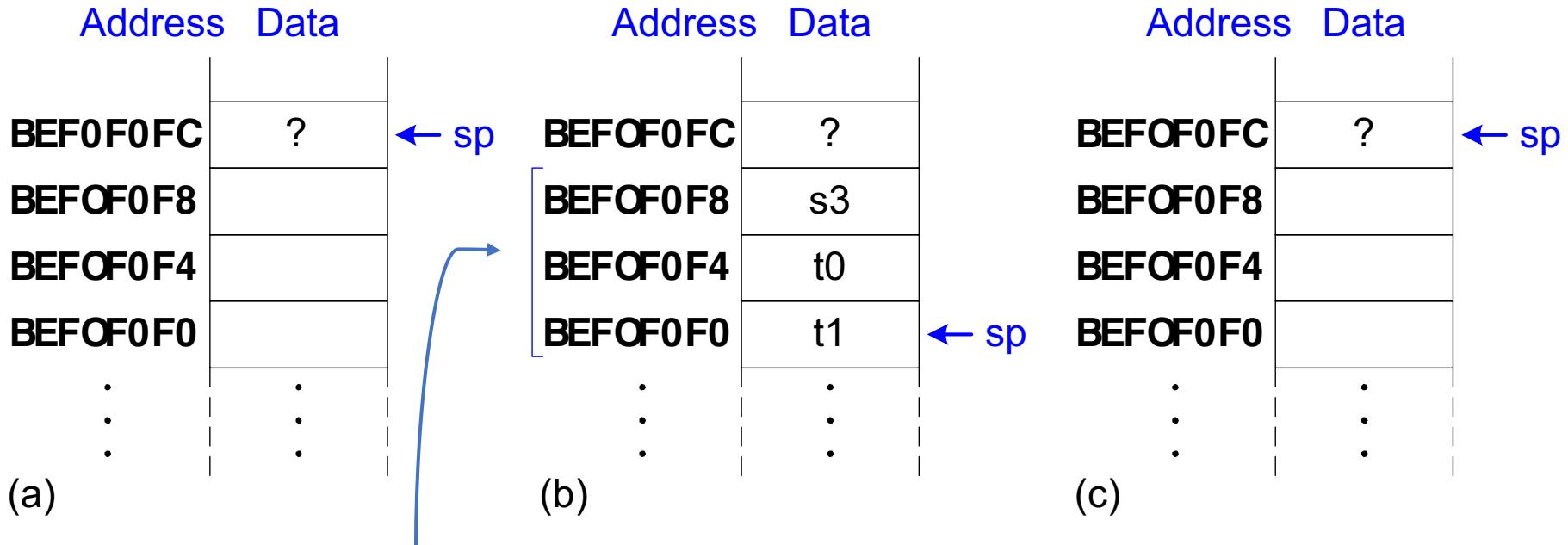
---

```
jr ra
```

```
# make space on stack to
# store three registers
# save s3 on stack
# save t0 on stack
# save t1 on stack
# t0 = f + g
# t1 = h + i
# result = (f + g) - (h + i)
# put return value in a0
# restore $t1 from stack
# restore $t0 from stack
# restore $s3 from stack
# deallocate stack space
# return to caller
```



# The Stack During `diffofsums` Call



# Register Saving Conventions

Preserved <i>Callee-Saved</i>	Nonpreserved <i>Caller-Saved</i>



# Storing Saved Registers on the Stack

```
# s3 = result
diffofsums:
_____
add t0, a0, a1          # make space on stack to
add t1, a2, a3          # store one register
sub s3, t0, t1          # save s3 on stack
add a0, s3, zero         # t0 = f + g
_____
# t1 = h + i
# result = (f + g) - (h + i)
# put return value in a0
# restore s3 from stack
# deallocate stack space
# return to caller
_____
jr ra
```

# Optimized diffofsums

```
# a0 = result
diffofsums:
    add t0, a0, a1      # t0 = f + g
    add t1, a2, a3      # t1 = h + i
    sub a0, t0, t1      # result = (f + g) - (h + i)
    jr ra               # return to caller
```

# Non-Leaf Function Calls

Non-leaf function:

---

func1:

---

---

# make space on stack

# save ra on stack

jal func2

...

---

---

# restore ra from stack

# deallocate stack space

# return to caller

jr ra

# Function Call Summary

- **Caller**

- Put arguments in \_\_\_\_\_
- Save any needed registers (\_\_\_\_\_)
- Call function: \_\_\_\_\_
- (Possibly restore registers)
- Look for result in \_\_\_\_\_

- **Callee**

- Save registers that might be disturbed (\_\_\_\_\_)
- Perform function
- Put result in \_\_\_\_\_
- Restore registers
- Return: \_\_\_\_\_

# Recursive Functions

```
int factorial(int n) {  
    if (n <= 1)  
        return 1;  
    else  
        return (n * _____);  
}
```

# Recursive Functions

```
0x8500 factorial: addi sp, sp, -8      # make room for a0, ra
0x8504             sw   a0, 4(sp)
0x8508             sw   ra, 0(sp)
0x850C             addi t0, zero, 1      # temporary = 1
0x8510             bgt a0, t0, else    # if n>1, go to else
0x8514             addi a0, zero, 1      # otherwise, return 1
0x8518             addi sp, sp, 8       # restore sp
0x851C             jr   ra           # return
0x8520 else:        addi a0, a0, -1      # n = n - 1
0x8524             jal  factorial      # recursive call
0x8528             lw   ra, 0(sp)      # restore ra
0x852C             lw   t1, 4(sp)      # restore n into t1
0x8530             addi sp, sp, 8       # restore sp
0x8534             mul a0, t1, a0      # a0 = n*factorial(n-1)
0x8538             jr   ra           # return
```

# Recursive Functions

What does the stack look like when executing factorial(3) ?

```
0x8500 factorial: addi sp, sp, -8
0x8504           sw   a0, 4(sp)
0x8508           sw   ra, 0(sp)
0x850C           addi t0, zero, 1
0x8510           bgt  a0, t0, else
0x8514           addi a0, zero, 1
0x8518           addi sp, sp, 8
0x851C           jr   ra
0x8520 else:     addi a0, a0, -1
0x8524           jal   factorial
0x8528           lw    ra, 0(sp)
0x852C           lw    t1, 4(sp)
0x8530           addi sp, sp, 8
0x8534           mul  a0, t1, a0
0x8538           jr   ra
```

Address Data

**BEFF0FF0**

**BEFF0FEC**

**BEFF0FE8**

**BEFF0FE4**

**BEFF0FE0**

**BEFF0FDC**

**BEFF0FD8**

# Recursive Functions

Stack (a) before, (b) during, and (c) after recursive call.

