

Introduction to Computer Engineering (E85)

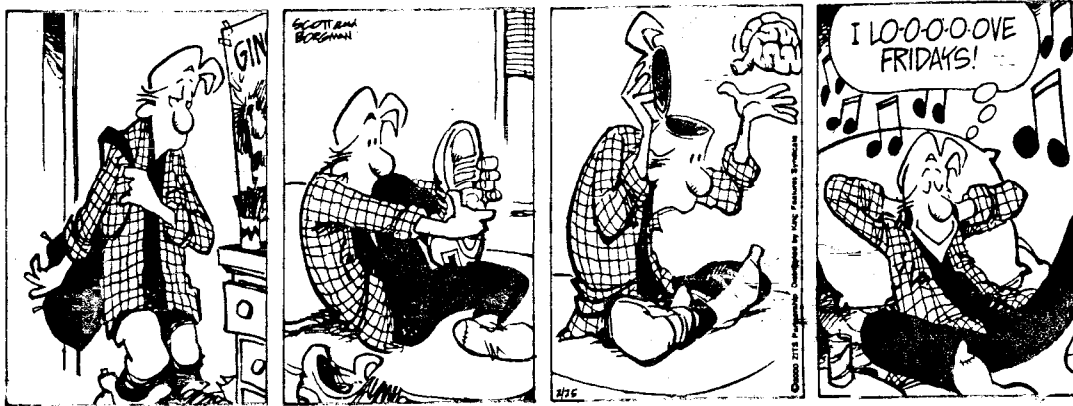
Harris

Fall 2010

Problem Set 9

Due: Wednesday, November 17

ZITS By Jerry Scott and Jim Borgman



1) Textbook Problems

Do problems 7.8, 7.11(a, e), 7.15, 7.20.

For 7.8, consider the R-type instructions (add, sub, and, or, slt), lw, sw, beq, j, addi.

Two copies of Figure 7.27 and 7.39 are attached for convenience on Exercise 7.11.

2) Time

Please indicate how many hours you spent on this problem set. This will not affect your grade, but will be helpful for calibrating the workload for next semester's class.

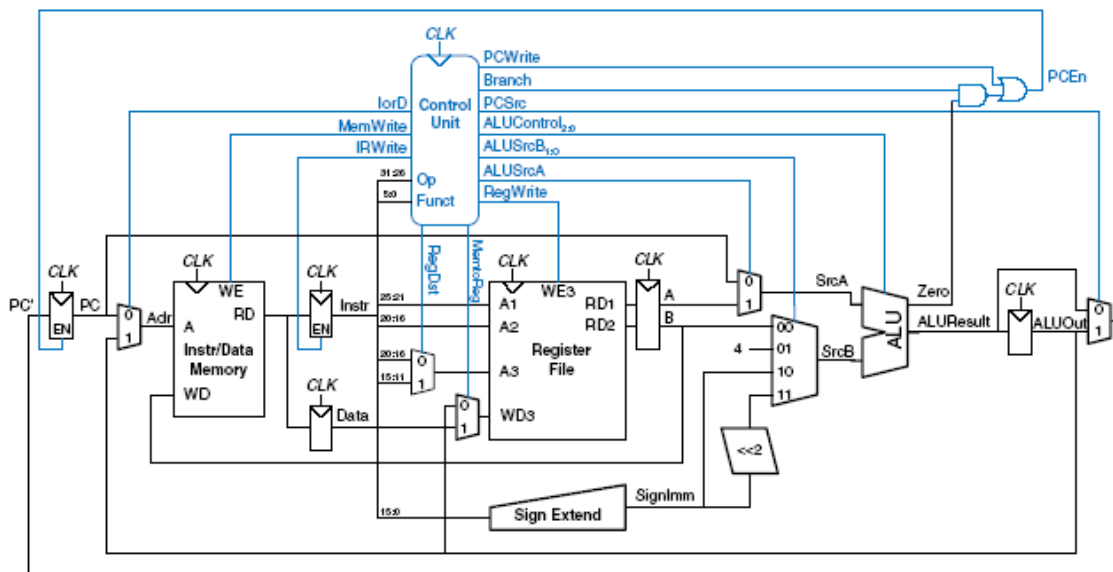


Figure 7.27 Complete multicycle MIPS processor

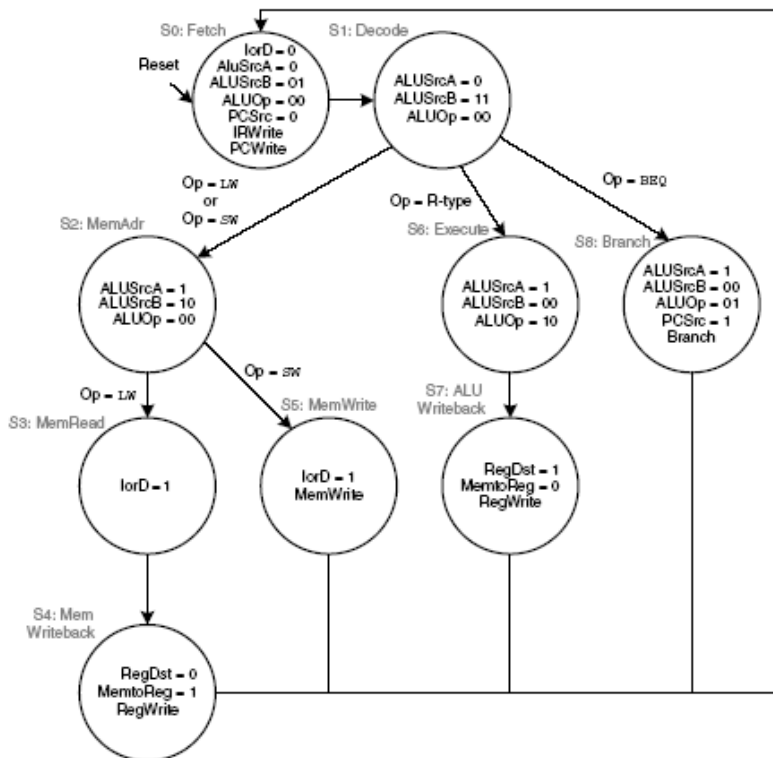


Figure 7.39 Complete multicycle control FSM

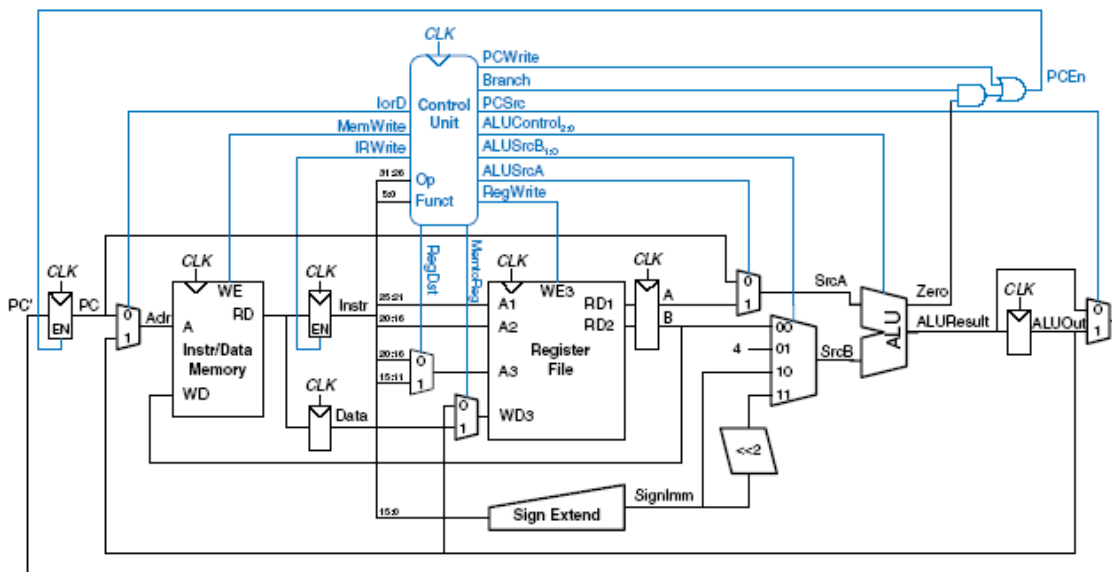


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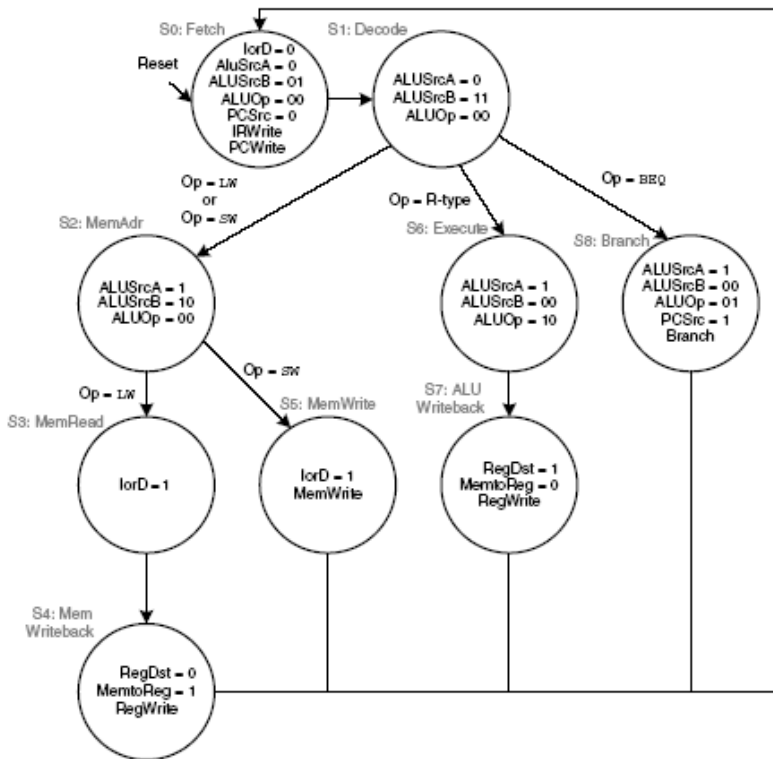


Figure 7.39 Complete multicycle control FSM