Introduction to Computer Engineering (E85)

Harris   Fall 2007

Problem Set 2   **Due: Wednesday, September 19**

1) Textbook Problems

Do problems 1.54, 1.61, 1.65, 2.4(c-d), 2.16, 2.18, 2.24, and Interview Question 2.2.

2) Tristate Inverter

Design a tristate inverter using CMOS transistors. The inputs are A, EN, and $\overline{EN}$. The output is Y. Use no more transistors than necessary.

3) Time

Please indicate how many hours you spent on this problem set. This will not affect your grade, but will be helpful for calibrating the workload for next semester's class.