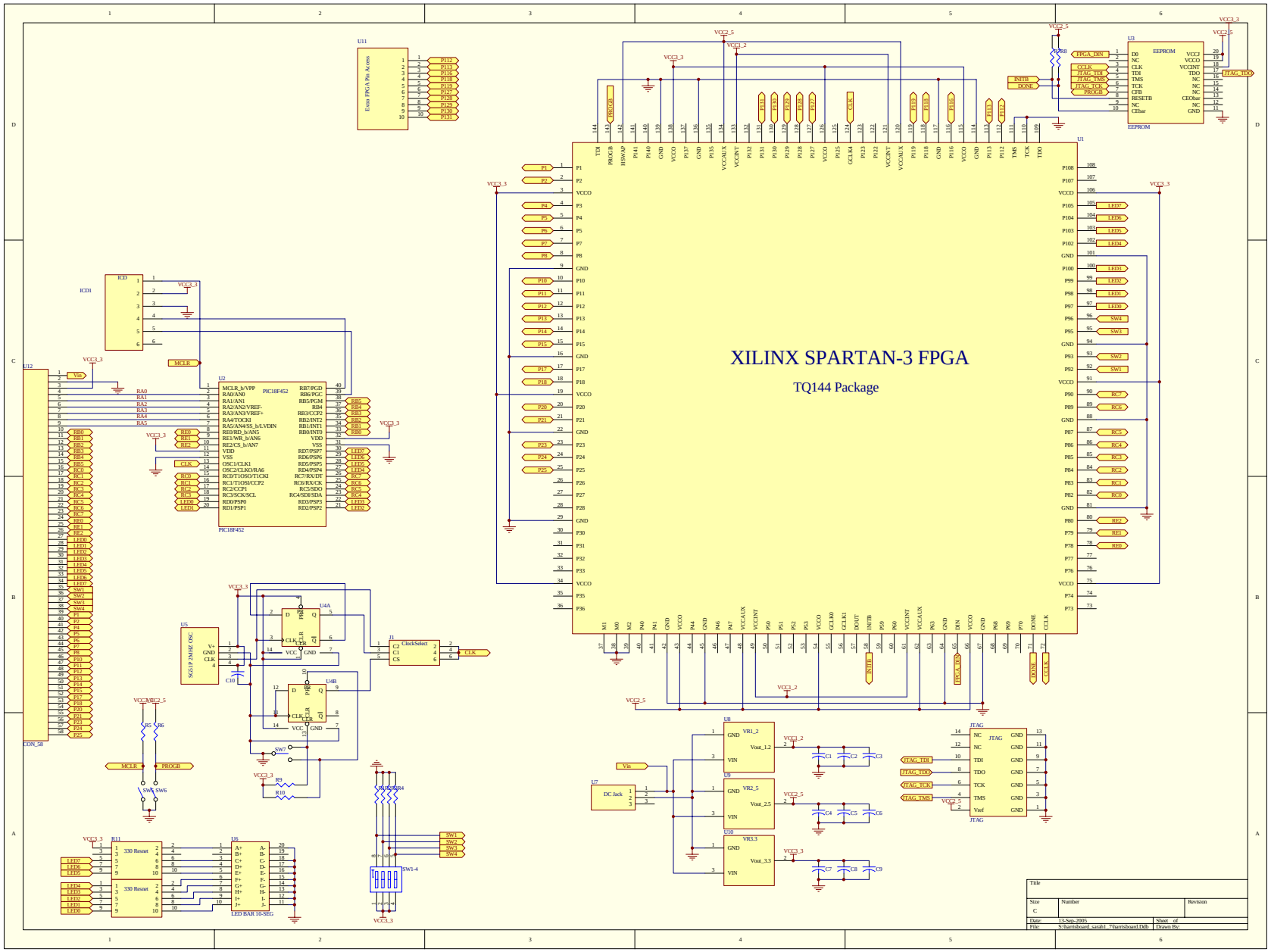
XILINX SPARTAN-3 FPGA
TQ144 Package
U1
U2
PIC18F452
U3
EEPROM
U4A
U4B
U5
SG310P 2MHZ OSC
U6
LED BAR 10-SEG
U7
DC Jack
U8
U9
U10
VR1_2
VR2_5
VR3.3
U11
Extra FPGA Pin Access
U12
CON_58
ICD1
ICD
J1
ClockSelect
JTAG
SW1-4
Title
Size
C
Number
Revision
Date: 13-Sep-2005
Sheet of
File: S:\harrisboard_sarah1_7\harrisboard.Ddb
Drawn By: